

Notice of References Cited		Application/Control No.	Applicant(s)/Patent Under Reexamination	
		10/016,693	LAMMERT, MICHAEL D.	
Examiner		Art Unit		Page 1 of 1
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*	D	US-6,153,525	11-2000	Hendricks et al.	438/692
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	U	Wolf, Stanley, Silicon Processing for the VLSI Era, Volume 2: Process Integration; 1990 by Lattice Press, pages 214-215
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.